

REMARKS

Annotated and replacement drawing sheets are enclosed as required.

Wherefore, a favorable action is earnestly solicited.

Respectfully submitted,

KIRSCHSTEIN, OTTINGER, ISRAEL & SCHIFFMILLER, P.C.

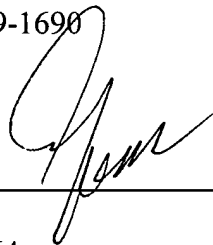
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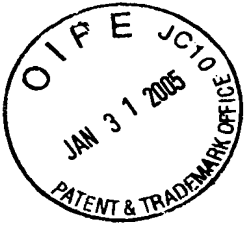


Alan Israel

Reg. No. 27,564

DRAWING AMENDMENTS

Please amend FIGS. 1-4 by adding the legend "*Prior Art*" as shown in the annotated drawing sheets following page 3.



u
ANNOTATED

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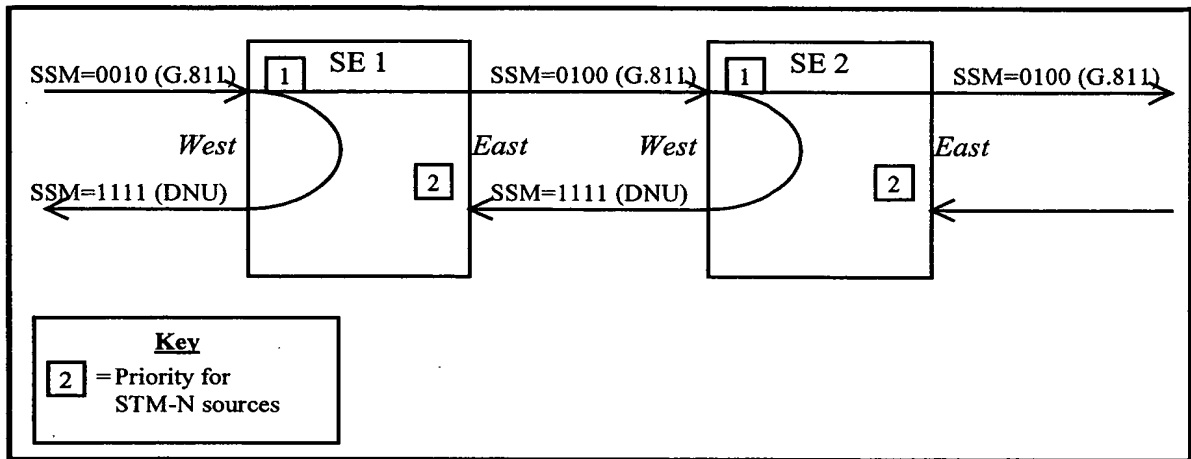


Figure 1 Timing Loop Prevention **PRIOR ART**

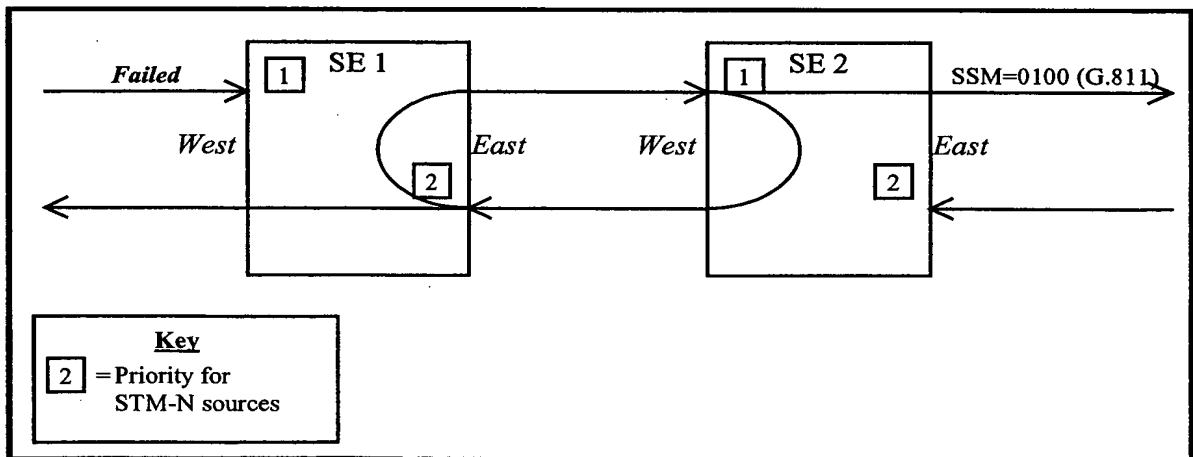


Figure 2 Synchronisation Timing Loop **PRIOR ART**



ANNOTATED

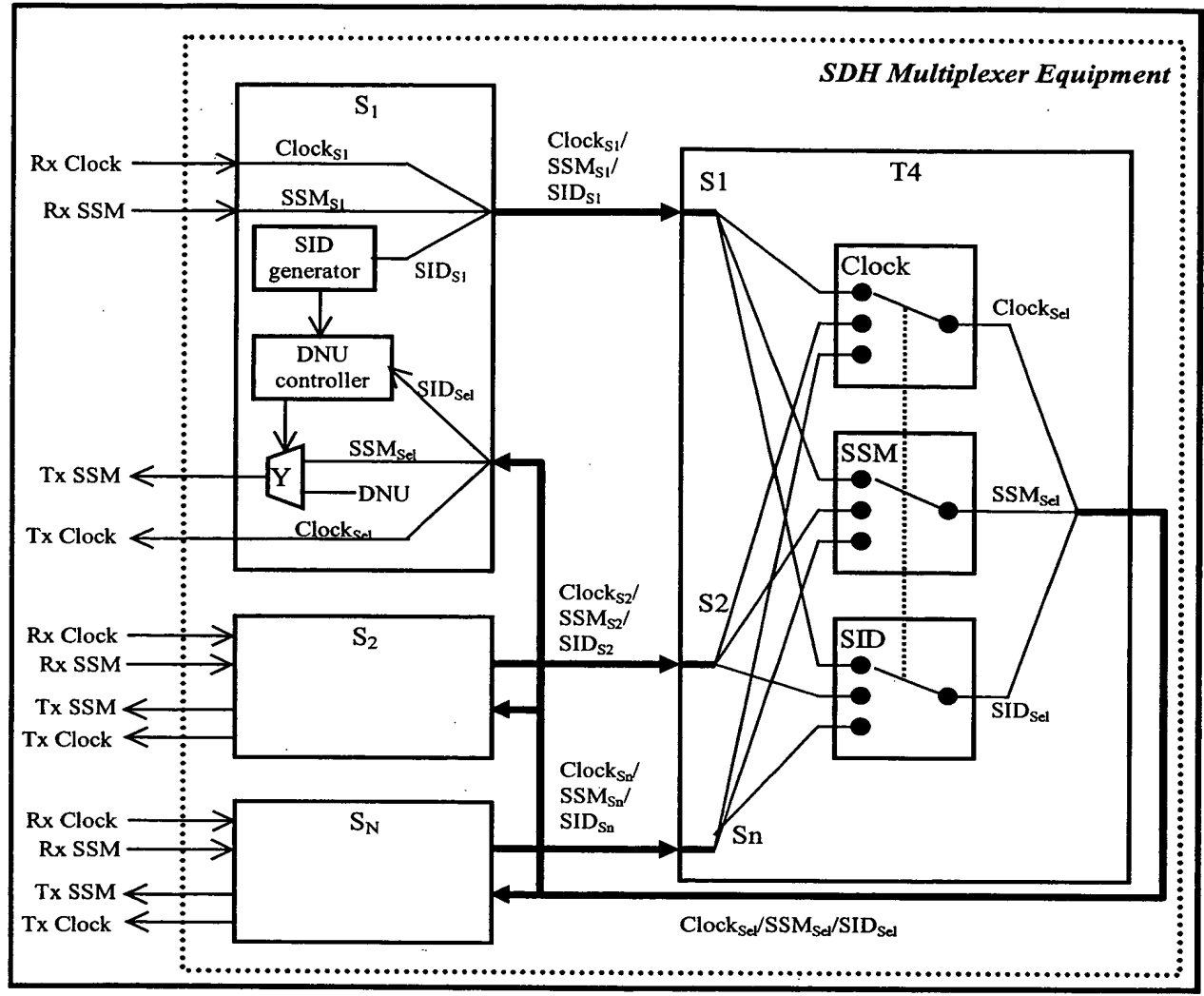
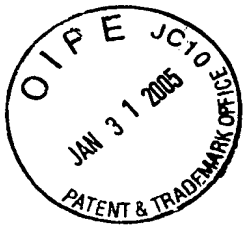


Figure 3 General use of SID in Timing Loop Prevention **PRIOR ART**



ANNOTATED

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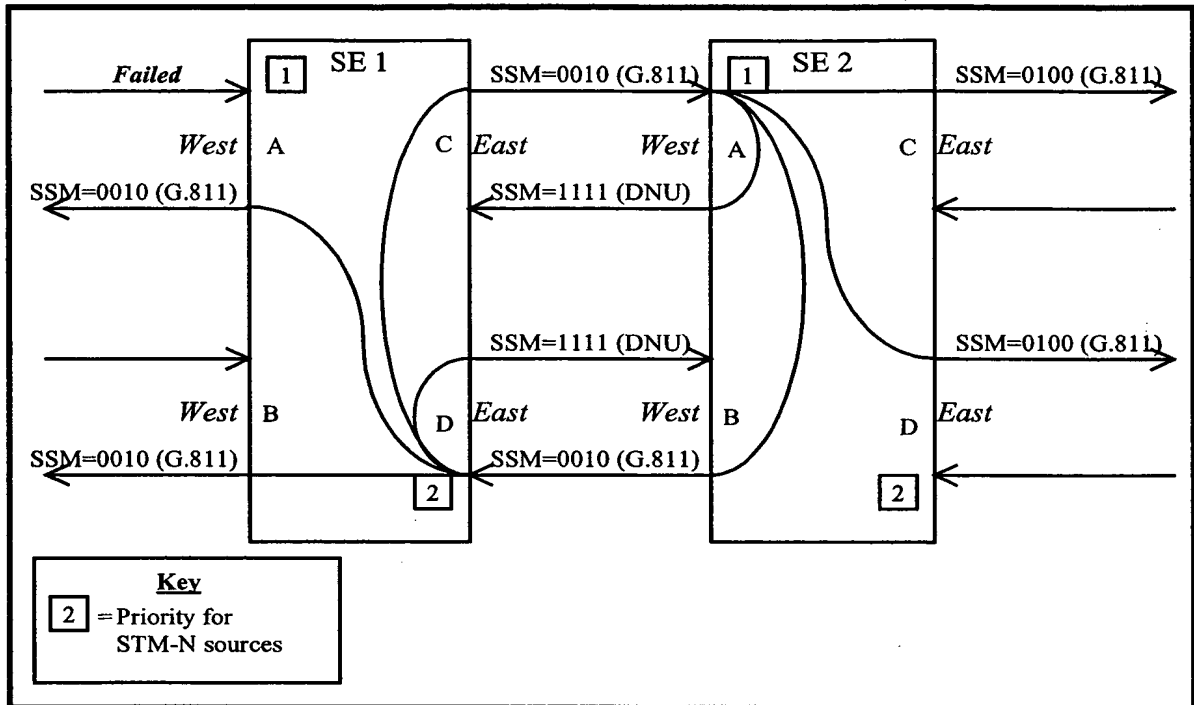


Figure 4 Multiple Port Timing Loop **PRIOR ART**

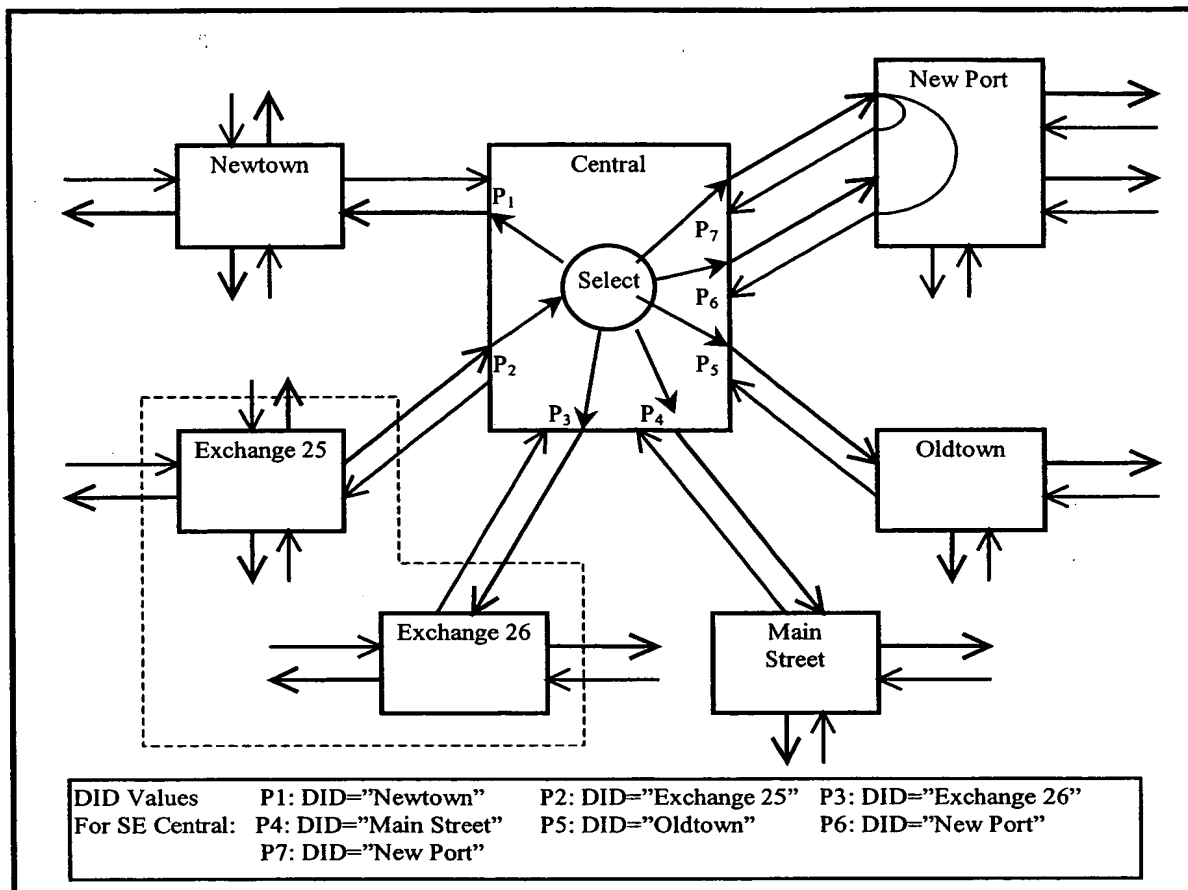


Figure 5 Destination IDs